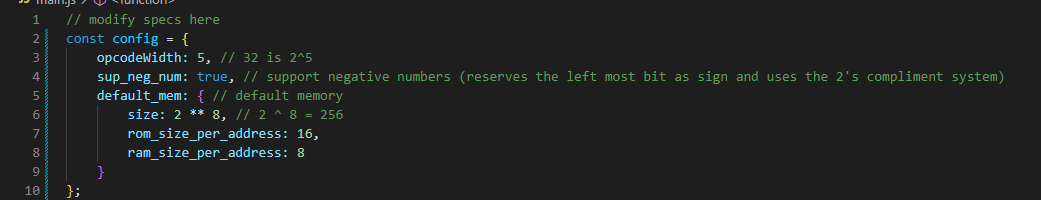
All modifications should be done inside the main.js file

**CONFIGURATIONS**



opcodeWidth

How many bits does the opcode occupy

sup\_neg\_num

Set to true if negative numbers (2’s compliment form) is needed else false

default\_mem

Contains the default memory values, can always be changed on memory specs in the top right corner of the simulator

Beyond this point, all items are found inside the IIFE (Immediately Invoked Function Expression) after the creation of the config object.

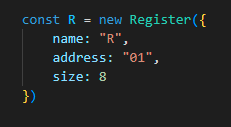
**REGISTERS**

Creating/Modifying Registers

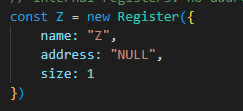
To create registers, you only need to create an instance of it and pass an object with the properties below. It is essential that the new instance is stored in a variable as this will become convenient in some cases when creating instructions.

* Name
  + *Name of the register*
* Address
  + *It’s binary designation or representation*
* Size
  + *Number of bits it can hold*

Example:



NOTE: Registers with no binary representation must have an address of NULL



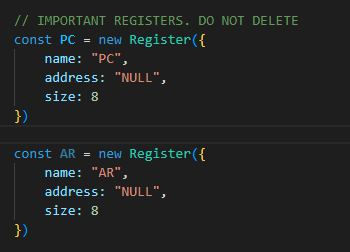
Change properties to modify a register

Removing Registers

Just delete the creation of instance of the register

**IMPORTANT REGISTERS**

Some registers are important and any modification or removal will cause the program to not work properly



(Although you can change its size to config.default\_mem.size)

**INSTRUCTIONS**

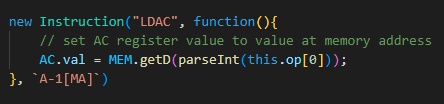
Creating/Modifying Instructions

When creating instructions, just create an instance of Instruction Class and it will automatically add on the simulator. There’s no need to store the new created instance in a variable.

When creating a new instance of the Instruction Class, it requires 3 parameters:

* Instruction Name
* Instruction Logic (in a form of function)
* Instruction Machine Code Format (optional)

Example:



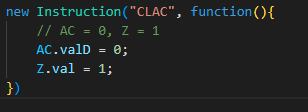
Instruction Name is straight forward, so let’s proceed on logic

INSTRUCTION LOGIC

Just like in Arduino, however, much easier.

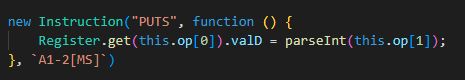
* Getting the operands
  + To access the operands, just use the this.op keyword, which is an array. The first index (0) contains the first operand, the second contains the second operand and so on…
  + NOTE: operands stored in the this.op are strings so if the inputted value is a number, be sure to use the parseInt function (if you intend it to be used as a number)
* Reading/Writing to a Register
  + If the register is already known (not specified in an operand) then just access the variable that it was stored on when creating it. This is why it is essential to store registers in a variable.

Example:



* + If not then use the static get method in the Register class and pass the name of the register, in this case, its name is in the operand

Example:

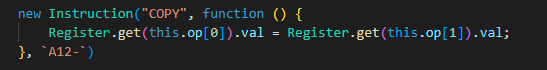


Setting the value of a register specified in the first operand to whatever the value passed in the second operand

* + Registers have 2 ways of accessing/writing its values. The valD and val properties
    - val

val is the immediate value of the register, whatever you put/read in here is reflected to the simulator, and it is intended that if ever you set this property to a new value, you set it as binary in a form of a string

Example:

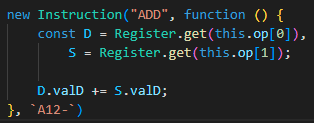


Whatever the value of the register at second operand is copied to the register at first operand

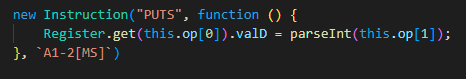
* + - valD

valD is made to read/modify the value of a register as a decimal, in cases where you want to put a decimal value but since register uses binary system, you would want to use the valD property, or in cases where you want to read the value as decimal to perform operations since you can’t do arithmetic on strings (because binary is represented here as strings)

Example:



Incrementing the value of register at operand 1 by the value of the register at operand 2



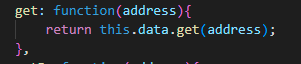
Setting the value of the register at operand 1 to whatever immediate decimal value in the operand 2, however, if the operand 2 is intended to be binary, then parseInt is not needed and the val property should be used instead

* Reading/Writing to the Memory

To access the Memory Object, the MEM keyword is used. Helper methods below:

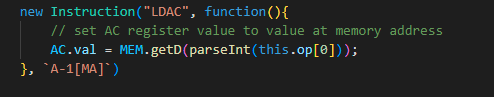
* + Reading from memory

Use the get method to get values in the memory. It requires an address parameter, which here is in binary form (that I do not suggest)



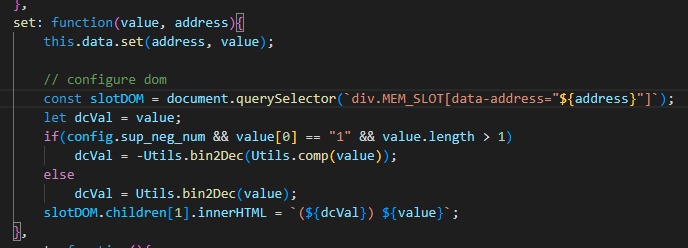
This is only the implementation. No examples because passing an address in binary form as an operand is unfriendly for mental health

In cases where the address is in decimal (type number), then use the getD method.



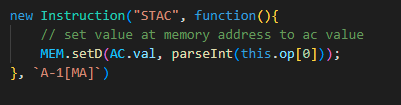
* + Writing to memory

Use the set method to set the values in the memory. It requires the value (in binary form this time) and the address, in this method, in binary form (that I do not suggest)



This is the implementation. Do not mind the code after the configure dom comment as this is only used for updating the memory values in the GUI

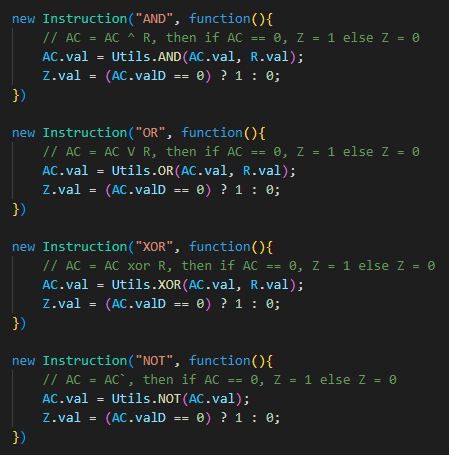
In cases where the address is in decimal form, use the setD method instead



* Utility Functions

Utility functions are found in the utils.js, these functions are used for easier processing of values when making complex instructions

Example



INSTRUCTION MACHINE CODE FORMAT

The third and an optional argument is the machine code format string. This format is used for proper arrangement of bits when “assembling” the assembly code into machine code

The default (if no value is passed) is



The machine code format has 3 types of characters

* A

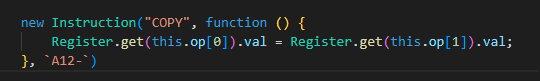
The A character is replaced with the opcode of the instruction.

* Numbers (range from 1 – 3)

The numbers correspond to the operands. 1 is the first operand, 2 is the second operand and so on. If ever your design has 9+ operands (2 digits+), please do not use the simulator

* “ – “

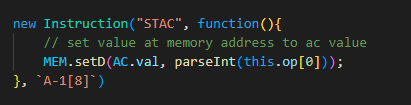
The excess character ( - ) is replaced if the number of bits used by the opcode and operands combined is less than the number of bits of a ROM memory. This is represented as “X” or don’t care in previous lectures



In here, the first and second operands are register and is arrange that they are beside each other after the opcode, the rest of the excess bits are “X” or don’t cares

In cases of constant number of bits used by an operand in the machine code, you can put a “[number of bits]” right after the operand number. In cases of registers, there is no need since creating register already provides its address

Example:

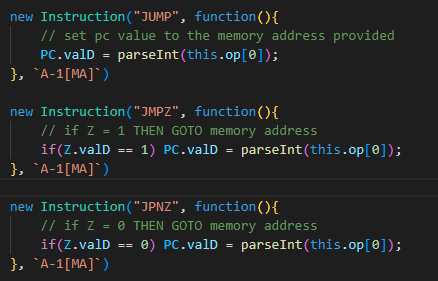
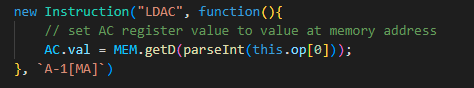


In the example above, the first operand has “[8]” right after it. This means that this operand will always contain 8 bits no matter how big or small the number is.

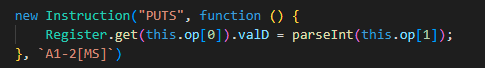
For convenience, the keyword MS and MA can be used to indicate MemorySize and MemoryAddress

MemorySize means the amount of bits that a single memory of RAM can contain

MemoryAddress means the amount of bits of an address in RAM or ROM

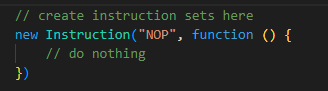


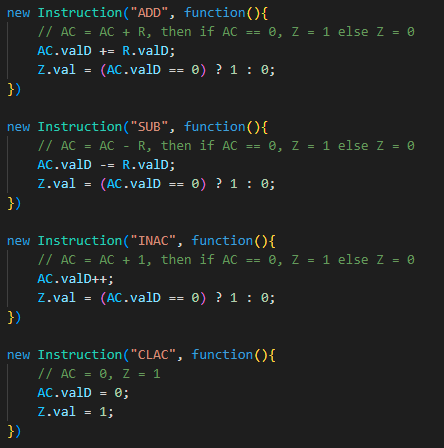
In here, the first operands are expected to be the same number of bits with MemoryAddress as the operand are indeed a memory address in the RAM (LDAC) and ROM (Jump Operations)



In here, the 2nd operand is an immediate value, that is to say that it has the same number of bits of each memory slot in the RAM.

In cases where there are no operands, you can omit the argument

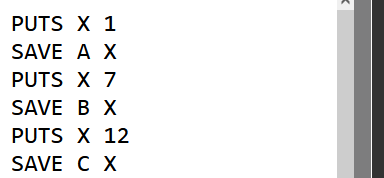




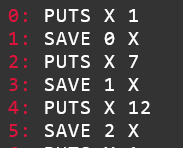
Machine code format is optional if the machine code feature has no use.

**“COMPILER”**

* The use of variables that signifies memory address in RAM can be used. Each variable are assigned its own address automatically



Converted to:

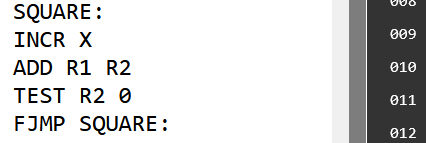


A ⟶ 0

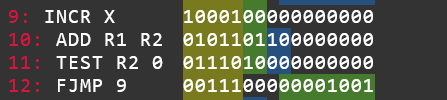
B ⟶ 1

C ⟶ 2

* The use of markers to indicate parts of the program can be used, but they require the colon “:” character right after to access/create them



Converted to:



**DEBUGGING**

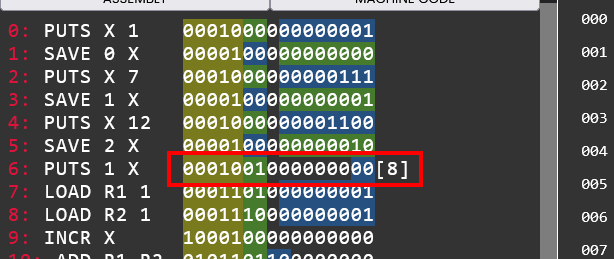
When the simulator is not working properly look for these signs:

* A register is saved as a memory address with a comma in it



The simulator prohibits the use of comma to separate operands, use only 1 space

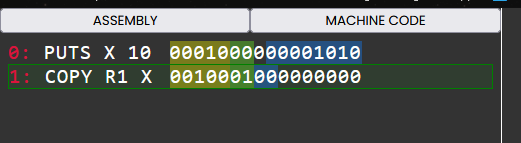
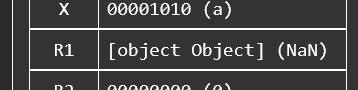
* A “[number]” appeared in the machine code



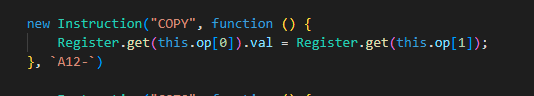
Caused by disordered operands, in the example above, first operand should be a register and the second operand should be an immediate value, not the other way around. If the symptom shows even if the order is correct, then please check the instruction logic in main.js

* A wild “[object Object]” has appeared

When assigning values in the instruction logic, make sure you are assigning it to a value, not the object

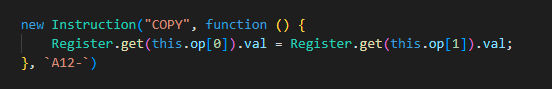


Is caused by



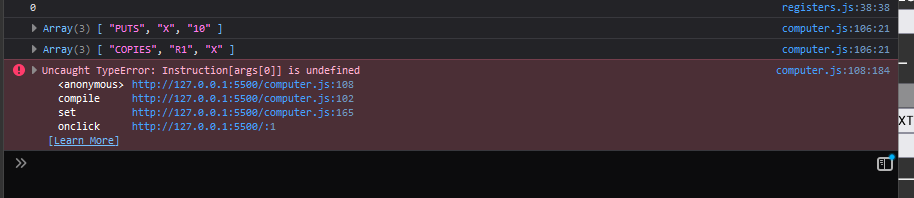
Setting the value of register at 1st operand to the register object of the register at 2nd operand

Fix:



Added the val property to access the value of the register

* After all of that, code is still not working properly

Check the console in the dev tools (Press CTRL+SHIFT+I for Mozilla and Chrome browsers) and check for this error:  


Instruction[args[0]] is undefined, this usually means that the logged information right before this error contains an instruction that is not created, thus it being undefined. As you can see above, the logged info right after the error contains an instruction with a name of “COPIES” with operands of R1 and X. There is no such thing as “COPIES” in my ISA, thus, the reason of the error.

* Still not working properly

Email me [jkdimpas@gmail.com](mailto:jkdimpas@gmail.com) or just message me in my facebook <https://www.facebook.com/josh.dimpas.9/>